IN THE CLAIMS

Please amend the claims as follows:

Claim 1 (Currently Amended): A display apparatus comprising:

a panel including a plurality of gate lines extending along rows, a plurality of signal lines extending along columns, a plurality of pixels arranged in a matrix at intersecting points at which said gate lines and said signal lines intersect with each other, and a plurality of image lines separated into a plurality of systems for supplying an image signal;

a vertical driving circuit connected to said gate lines for successively selecting the rows of said pixels;

a plurality of sampling switches disposed for connecting said signal lines to said image lines; and

a horizontal driving circuit operable in response to a clock signal for successively generating sampling pulses to successively drive said sampling switches so that the image signal is successively written into the pixels of the selected row;

said horizontal driving circuit applying ~~double sampling~~ two pulses including a precharging pulse ~~a first pulse~~ and a sampling pulse ~~a second pulse~~ to each of said sampling switches such that the corresponding signal line is precharged with the image signal in response to the ~~first~~ precharging pulse and then the image signal is sampled to the signal line in response to the ~~second~~ sampling pulse;

said image lines being connected such that, ~~where~~ when the ~~second~~ sampling pulse of ~~double sampling~~ two pulses applied to a ~~preceding~~ first one of said sampling switches and the ~~first~~ precharging pulse of ~~double sampling~~ the two pulses applied to a ~~succeeding~~ second one of said sampling switches are in a temporarily overlapping relationship with each other, ~~different ones of said~~ image lines of a first system are connected to the ~~preceding~~ first sampling switch and image lines of a second system are connected to the ~~succeeding~~ second

sampling switch thereby to prevent interference of the image signal between the ~~two~~ first and second sampling switches due to overlapping of the precharging and sampling pulses.

Claim 2 (Currently Amended): A display apparatus according to claim 1, wherein said horizontal driving circuit includes a shift register for receiving a clock signal having a predetermined period and a start pulse having a pulse width equal to twice the predetermined period and performing a shifting operation of the start pulse in synchronism with the clock signal to successively output shift pulses from individual shift stages thereof and an extraction switch set for extracting a clock signal having the same period as that of the clock signal having a predetermined period in response to the shift pulses successively outputted from said shift register to successively produce the ~~double-sampling~~ two pulses.

Claim 3 (Currently Amended): A display apparatus according to claim 2, wherein the image line of a first system is connected to those of said sampling switches which belong to a first group in which the sampling switches are disposed at every third place and the image line of a second system is connected to those of said sampling switches displaced by a one-switch distance from the sampling switches of the first group while the image line of a third system is connected to those of the sampling switches of the remaining third group thereby to prevent interference of the image signal between the ~~preceding~~ first sampling switch and the ~~succeeding~~ second sampling switch.

Claim 4 (Currently Amended): A driving method of a display apparatus which includes a panel including a plurality of gate lines extending along rows, a plurality of signal lines extending along columns, a plurality of pixels arranged in a matrix at intersecting points at which said gate lines and said signal lines intersect with each other, and a plurality of

image lines separated into a plurality of systems for supplying an image signal, a vertical driving circuit connected to said gate lines for successively selecting the rows of said pixels, a plurality of sampling switches disposed for connecting said signal lines to said image lines, said sampling switches including a first sampling switch and a second sampling switch, and a horizontal driving circuit operable in response to a clock signal for successively generating sampling pulses to successively drive said sampling switches so that the image signal is successively written into the pixels of the selected row, comprising:

~~a step executed~~ executing by said horizontal driving circuit of applying ~~double sampling~~ two pulses including a ~~first~~ precharging pulse and a ~~second~~ sampling pulse to each of said sampling switches such that the corresponding signal line is precharged with the image signal in response to the ~~first~~ precharging pulse and then the image signal is sampled to the signal line in response to the ~~second~~ sampling pulse; and

~~a step of~~ connecting, where the ~~second~~ sampling pulse ~~of double sampling pulses~~ applied to ~~a preceding~~ the first ~~one of said~~ sampling ~~switches~~ switch and the ~~first~~ precharging pulse ~~of double sampling pulses~~ applied to ~~a~~ the second ~~succeeding one of said~~ sampling ~~switches~~ switch are in a temporarily overlapping relationship with each other, ~~different ones of said~~ image lines of a first system to the ~~preceding~~ first sampling switch and image lines of a second system to the ~~succeeding~~ second sampling switch thereby to prevent interference of the image signal between the two sampling switches because of overlapping pulses.

Claim 5 (New): A method of driving an active matrix display comprising:

applying two pulses, a precharging pulse and a sampling pulse, to sampling switches of a horizontal driving circuit such that the sampling pulse is applied to a first sampling switch of a first image line system at a same time that the precharging pulse is applied to a second sampling switch of a second image line system.

Claim 6 (New): A display apparatus comprising:

a panel configured to include a plurality of gate lines extending along rows, a plurality of signal lines extending along columns, a plurality of pixels arranged in a matrix at intersecting points at which said gate lines and said signal lines intersect with each other, and a plurality of image lines separated into a plurality of systems for supplying an image signal;

a vertical driving circuit configured to connect to said gate lines and successively select the rows of said pixels;

a plurality of sampling switches configured to connect said signal lines to said image lines, said plurality of sampling switches including a first sampling switch and a second sampling switch; and

a horizontal driving circuit configured to generate, in response to a clock signal, sampling pulses to successively drive said sampling switches so that the image signal is successively written into the pixels of the selected row;

said horizontal driving circuit configured to apply two pulses including a precharging pulse and a sampling pulse to each of said sampling switches such that the corresponding signal line is precharged with the image signal in response to the precharging pulse and then the image signal is sampled to the signal line in response to the sampling pulse;

said image lines being connected such that, when the sampling pulse of two pulses applied to the first sampling switch and the precharging pulse of two pulses applied to the second sampling switch are in a temporally overlapping relationship with each other, image lines of a first system are connected to the first sampling switch and image lines of a second system are connected to the second sampling switch thereby to prevent interference of the image signal between the first sampling switch and the second sampling switch due to overlapping of the precharging and sampling pulses.